	Application No.	Applicant(s)	
Notice of Allowability	09/899,192	KISAICHI, TAKASH	
	Examiner	Art Unit	
	Maureen G. Arancibia	1763	
The MAILING DATE of this communication appeall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to	olication. If not include will be mailed in due	ed course. <b>THIS</b>
1.   This communication is responsive to the reply filed on 02/1	<u>14/2005</u> .		
2. ☑ The allowed claim(s) is/are <u>8,13-16 and 24-31</u> .			
3. 🔀 The drawings filed on <u>14 February 2005</u> are accepted by the Examiner.			
4.			
Attachment(s)  1. Notice of References Cited (PTO-892)  2. Notice of Draftperson's Patent Drawing Review (PTO-948)  3. Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date  4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. Notice of Informal P 6. Interview Summary Paper No./Mail Dat 08), 7. Examiner's Amendr 8. Examiner's Stateme 9. Other	(PTO-413), te nent/Comment	·
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## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2. The application has been amended as follows:

Please correct the spelling of the title to:

SEMICONDUCTOR MANUFACTURING APPARATUS, AND POSITIONING JIG USED FOR SAME

In the Specification, Page 5, Line 19:

Delete the duplicate phrase "of the s t" between "of the" and "structure."

## **REASONS FOR ALLOWANCE**

- 3. Claims 8, 13-16, and 24-31 are allowed.
- 4. The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach a semiconductor manufacturing apparatus, comprising a wafer support with a tapered lateral side supporting an edge of a wafer from below, a stage, wafer clamps, a holder accommodating said clamps, and a seat accommodating said holder, wherein male threads are formed at an outside surface of said holder and female threads that mesh with said male threads are formed at an inside surface of said seat. The prior art of record also fails to teach a semiconductor manufacturing apparatus, comprising a wafer support with a tapered lateral side supporting an edge of a wafer from below, a stage, wafer clamps, and a positioning jig to be employed for

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positioning said wafer clamps, the jig comprising a recess fitting that fits onto said stage and a lateral side that comes into contact with the wafer clamps to specify their position. The prior art of record further fails to teach methods of securing a wafer employing the above-described apparatuses.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## CONCLUSION

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Maureen G. Arancibia whose telephone number is (571) 272-1219. The examiner can normally be reached on core hours of 10-5, Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Parviz Hassanzadeh can be reached on (571) 272-1435. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Maureen G. Arancibia

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